



Dear Patrons,

January 2026 has set a decisive tone for the year ahead, marked by strong policy momentum, deeper state engagement, global outreach, and active industry participation across India's semiconductor and ESDM ecosystem.

The Government's approval of 22 projects under the third tranche of the Electronics Component Manufacturing Scheme (ECMS), representing ₹41,863 crore in committed investments, signals growing confidence in India's manufacturing capabilities. Complementing this progress, IESA continued engagement with state governments, including a strategic discussion with the **Government of Haryana** to support Centre of Excellence initiatives, cluster development, and semiconductor and EV value-chain investment strategies—further strengthening the foundation for regional ecosystem growth.

At the global level, IESA expanded international collaborations through engagements with delegations from **METI Kansai, Japan**, and **Enterprise Singapore**, advancing cross-border partnerships, talent development initiatives, and industry linkages. Reinforcing India's global positioning, IESA President **Mr. Ashok Chandak** addressed over 40 European companies in a webinar organised by the **Enterprise Europe Network (GBI)**, highlighting opportunities within India's rapidly evolving semiconductor ecosystem and key industry platforms such as SEMICON India and the IESA Vision Summit.

Industry participation and ecosystem outreach continued across national platforms. **Mr. Ashok Chandak, President – IESA, Mr. Sudhir Naik, Midwest Head – IESA**, along with other industry leaders, attended **SEMICONNECT Madhya Pradesh 2026**, held on 24 January 2026 in Indore. Mr. Naik spoke on the semiconductor sector's role in driving global and India's GDP growth, while IESA representatives **Mr. Rajeev Khushu and Mr. Dharmendra Singh Yadav** participated in the **National Conference on Strategic Trade Controls (NCSTC)**, held on 15 January contributing to discussions on export compliance and regulatory frameworks.

Alongside these engagements, IESA strengthened workforce readiness through focused knowledge sessions, including a Workforce Development Webinar featuring **Mr. Sivakumar P. R.** and a Technical Webinar on AI in semiconductor fabrication led by **NXP experts Mr. Mallikarjuna Bande and Ms. R. Reshma Krishnakumar**, supporting talent development and industry preparedness.

Together, these initiatives reflect a connected and forward-moving ecosystem—where policy alignment, global collaboration, regional engagement, and talent development are advancing in tandem to power India's semiconductor ambitions.

As we are gearing up for our flagship event, we invite all our stakeholders to join the **IESA Vision Summit 2026** to shape the next phase of India's semiconductor and ESDM journey, with continued industry participation and playing a critical role in building a globally competitive ecosystem.



Yours Sincerely
Ashok Chandak
President, IESA



Yours Sincerely
Ruchir Dixit
Chairman, IESA

IESA Media & PR Dashboard: January Highlights

Overall Media Reach (Metric Coverage)

Total Media Stories	126
Print	75
Online	48
TV Interview	3

Tier -1/ TV Media Visibility

Platform	Engagement
NDTV Profit	Exclusive Interaction
CNBC- TV 18	Panel opportunity
ET Swadesh	Exclusive Panel Discussion

Policy & Industry Narrative

Topic	Platform
Chip and EMS Companies under ISM 2.0	Economic Times
India- EU-FTA	Economic Times, Business Standard, ET Telecom, Business Today, ET Brand Equity, and Business World
Industry Features	The Hindu Business Line, Mint
Chinese Joint Ventures	Economic Times

Regional Media Impact

Focus Area	Coverage
Year-End Quote on ESDM Sectors	IANS Wire
Modernise SCL	ET Manufacturing, Mint, and Free Press Journal
UP Tech Next Electronics & Semiconductor Summit	CNBC-TV18
Industry Stories	Business Today

Social Media Growth

Platform	Performance
LinkedIn	+539 (Total 35,506)
Twitter/ X Engagement	1600 impressions, 467 engagements, 31 followers

Key Highlights

- IESA’s media engagement significantly strengthened policy visibility, industry credibility, and national and regional reach—reinforcing IESA as a trusted voice in India’s ESDM and semiconductor ecosystem.
- Ashok Chandak, President IESA, continues to serve as a key spokesperson for the ESDM industry and IESA, effectively articulating industry perspectives across leading national and regional media platforms.

[View PR Coverage Summary](#)

IESA President Highlights India's Semiconductor Progress in ET NOW Interview

In an exclusive interaction with ET NOW, IESA President Ashok Chandak highlighted India's accelerating progress in electronics and semiconductor manufacturing, emphasizing the transition from policy vision to on-ground execution. He noted key milestones, including multiple approved chip and electronic component projects, expanding assembly capacity, and landmark fabs underway. Mr. Chandak also acknowledged the continued support of MeitY initiatives, underscoring the importance of localisation, skilling, and innovation in strengthening India's globally competitive semiconductor ecosystem.



[Watch the full interview](#)



IESA Midwest Head Addresses BNI Olympus as Power Guest

Mr. Sudhir Naik, Midwest Head – IESA, was invited as a Power Guest at the BNI Olympus session on 9 January 2026, where he shared perspectives on India's evolving technology and semiconductor landscape. Highlighting the growing opportunities across startups, electronics, and semiconductor innovation, he emphasized the importance of industry collaboration and ecosystem development in shaping India's technology-driven future. The engagement reflected IESA's continued outreach to strengthen industry awareness and partnerships.

IESA Engages European Industry on Opportunities Under India-EU FTA

Following the conclusion of the India-EU Free Trade Agreement, IESA President Ashok Chandak addressed over 40 European companies on 29 January 2026 in a webinar organised by the Enterprise Europe Network (GBI). Titled "India's Emerging Semiconductor Landscape: Opportunities and Pathways for European Companies," the session highlighted India's evolving semiconductor ecosystem, export potential, and key platforms such as SEMICON India and the IESA Vision Summit, reflecting growing India-EU collaboration across electronics and semiconductors.

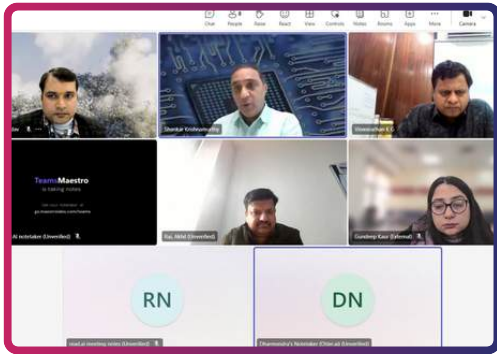


IESA Strengthening India-Japan ESDM Collaboration

IESA hosted a delegation from Japan's METI Kansai Bureau in Bengaluru to explore a Regional Collaborative Framework aimed at strengthening business, investment, and technology partnerships between Kansai-based SMEs and Indian companies. Discussions focused on B2B collaboration, talent development through trainer exchanges, and opportunities across semiconductor equipment, EVs, and advanced materials. The engagement reinforces IESA's role in enabling global partnerships that support India's expanding semiconductor and high-tech manufacturing ecosystem.

ECMS Approvals Accelerate India's Electronics Value Addition Momentum

On 2nd January, IESA welcomed the Government's approval of 22 proposals under the third tranche of the Electronics Component Manufacturing Scheme (ECMS), representing committed investments of ₹41,863 crore. Highlighting the milestone, IESA President Ashok Chandak noted that ECMS is strengthening domestic component ecosystems, enhancing value addition, and improving India's ESDM competitiveness. The approvals mark a significant step toward resilient electronics supply chains, increased exports, and positioning India as a globally investible destination for electronics and semiconductor manufacturing.



IESA Engages with Haryana Government on ESDM Ecosystem Development

IESA held a virtual discussion with the Government of Haryana on 22 December 2025 to advance collaboration on establishing a Centre of Excellence, cluster development, and support frameworks for the state's ESDM ecosystem. Discussions focused on developing a database of semiconductor and EV value-chain companies, attracting new investments leveraging Haryana's infrastructure strengths, identifying stakeholders for the proposed ESDM Park, and supporting upcoming Innovation Week initiatives and industry engagement programs.

IESA Participates in National Conference on Strategic Trade Controls 2026

IESA representatives participated in the National Conference on Strategic Trade Controls (NCSTC), held on 15 January 2026 in New Delhi, India's flagship platform on SCOMET, export controls, and compliance. Mr. Dharmendra Singh Yadav, Director – Government Relations and Policy, IESA, and EC Member Mr. Rajeev Khushu attended the conference, engaging in discussions on regulatory frameworks, licensing processes, and emerging technology trade compliance, reinforcing IESA's commitment to supporting responsible high-tech exports.



IESA Highlights Semiconductor Growth Opportunities at SEMICONNECT Madhya Pradesh 2026

Mr. Ashok Chandak, President – IESA, Mr. Sudhir Naik, Midwest Head – IESA, along with other industry leaders, attended SEMICONNECT Madhya Pradesh 2026, held on 24 January 2026 in Indore. During the event, Mr. Sudhir Naik delivered a presentation on the role of the semiconductor industry in driving global and India's GDP growth. The conference brought together industry, academia, and policymakers to position Madhya Pradesh as an emerging hub for electronics manufacturing and talent development, reinforcing IESA's commitment to strengthening regional semiconductor ecosystems.

Strengthening India-Singapore Collaboration Across the ESDM Ecosystem

IESA met with representatives from Enterprise Singapore to advance collaboration across the electronics and semiconductor ecosystem. Discussions focused on country roundtables at the IESA Vision Summit, potential Singapore pavilions at key IESA platforms, and joint initiatives in skill development and training for semiconductor professionals. The engagement also emphasized enabling cross-border partnerships for IESA member companies, reinforcing IESA's facilitative role in building stronger India-Singapore industry linkages.



Highlights from Workforce Development Webinar

IESA conducted a Workforce Development Webinar on "Building a Career in the Semiconductor Industry: Essential Skills and Insights" on 21 January 2026. The session featured Mr. Sivakumar P. R., Founder & CEO, Maven Silicon, who shared industry expectations, emerging skill requirements, and practical career pathways in the semiconductor ecosystem. The webinar attracted strong participation from students and young professionals, reinforcing the importance of workforce readiness as India accelerates its ESDM growth journey.



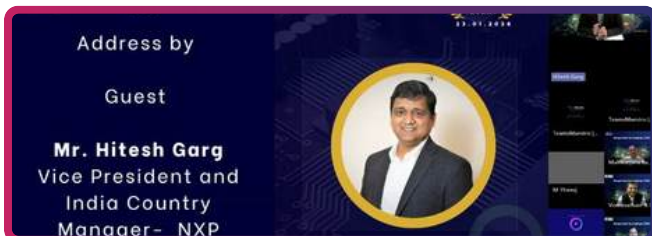
Insights from IESA Technical Webinar on AI in Semiconductor Chip Fabrication

IESA hosted a technical webinar on "Semiconductor Chip Fabrication Challenges and the Role of Artificial Intelligence" on 28 January 2026, bringing together industry professionals and technology enthusiasts. Experts from NXP, Mr. Mallikarjuna Bande, Director – Design Enablement & AI Digital Transformation, and Ms. R. Reshma Krishnakumar, Team Lead – IP Development, shared practical insights on how AI is improving fabrication efficiency, yield optimization, and process outcomes, highlighting the growing integration of AI across semiconductor manufacturing workflows.



Launch & Orientation Session Marks the Beginning of DeepTech Hackathon 2026

IESA successfully conducted the Launch and Orientation Session for the DeepTech Hackathon 2026 on 23 January 2026, virtually, formally initiating the national innovation program. Organised by IESA, with NXP Semiconductors as the industry sponsor and i4C as the official hackathon partner, the session outlined the hackathon structure, milestones, and problem statements. Representatives from IESA, NXP, and i4C engaged participants, setting the stage for collaborative deep-tech innovation.



UPCOMING EVENTS

- IESA NCR Chapter Meet, 4 February
- Webinar on “AI on the Edge: Deploying TinyML on Microcontrollers”, 4 February
- IESA–NXP Leadership Forum Roundtable, 12 February
- Knowledge session on Neural Network Optimization & Quantization”, 18 February
- Webinar on “Addressing Challenges of Impurities in the Semiconductor Supply Chain”, 19 February
- AI Impact Summit, 18 February

Plan Your Visit To IESA Vision Summit 2026

Step into one of the most anticipated gatherings of the year—IESA Vision Summit 2026, where the next phase of India's semiconductor and electronics transformation takes centre stage. Designed to bring together industry leaders, policymakers, innovators, startups, academia, and investors, the summit will offer powerful insights, meaningful collaborations, and forward-looking discussions shaping the journey from design leadership to large-scale manufacturing.

Event Highlights:

- 1,500+ Delegates representing the global semiconductor and electronics ecosystem
- 50+ Industry Leaders
- 10+ Senior Government Officials & Policymakers shaping national and global strategies
- Diverse Ecosystem Participation, including global and Indian corporates, MSMEs startups, incubators, and academia
- 100+ Booths showcasing cutting-edge products and innovations across the semiconductor and electronics value chain
- Exclusive Industry Report Launches offering key market and technology insights
- Industry & Innovation Awards recognizing excellence and leadership
- High-Impact Keynotes & Expert Panel Discussions
- International & National Roundtables enabling strategic dialogue and collaboration

VISION SUMMIT

Save the Date

25-26 FEBRUARY

TO KNOW MORE VISIT OUR WEBSITE

www.iesa-visionsummit.com

StartUp / MSMEs of the Month - February 2026



Sykatiya Technologies Pvt. Ltd.

Company Name: Sykatiya Technologies Pvt Ltd

Location: Bengaluru

Founder: Ram Jonnavithula & Bethanamudi Srinivas Rao

Website: www.sykatiya.com

Email ID: Info@sykatiya.com

Overview:

Sykatiya Technologies Pvt Ltd is a Bangalore-based IC design services company specializing in VLSI, Embedded, Automotive, AI, and Multi-Cloud technologies. Guided by experienced technology leaders, we foster a culture of innovation, ethics, and collaboration. As an active IESA member, we contribute to strengthening India's semiconductor ecosystem.

Key Products/ Services/Features

Strong expertise and able techno-leadership in all the domains from RTL Integration, Synthesis, DFT, and Physical Design come together to make sure customer design is owned, planned, executed and signed-off as per customer choice.

- Standard / Conventional Turnkey Execution
 - RTL – GDS & Technology Node with TSMC/GF/Samsung/Intel
- Value-Added Solutions
 - Ultra Low Power Optimization Technology
 - Seamless Integration
 - AI & Edge Devices
- Test - Chip Execution
 - Constraints Dev-Synthesis-DFT-Physical Design Architecture Definition
 - Tools: In-House
 - Sign-Off
- Standalone Solutions
 - Design Verification
 - DFT - TEST Architecture Definition & Implementation
 - Integration & create DFT, DV,PD, STA, Package & Test infrastructure
- Analog Design & Layout
 - End-to-End Analog Mixed Signal Design and Layout at the IP and SoC level
 - Power management, Temperature Sensors, and other complex Analog Mixed Signal Circui
- IC Design Services
 - Digital, Analog, Post Silicon Validation & Test & Product Engineering, Electrical Characteristics Compliance, High Speed Protocols, Functional Validation



Company Name: H2Loop AI Pvt. Ltd

Location: Bengaluru

Founder: Sairanjan Mishra and Pulkit Agarwal

Website: <https://www.h2loop.ai/>

Email ID: team@h2loop.ai

Overview:

H2Loop.ai is an AI-native systems engineering platform building the world's first suite of domain-specific Small Language coding Models (SLMs) for hardware, firmware, and system level software development. Designed for industries where reliability, safety, and performance are critical.

We make Hardware Aware AI for System Software Engineering Teams that are purpose-built for enterprise system software teams driving innovation in automotive, electronics, IoT, telecom, avionics, and semiconductors.

Features:

- I.HW aware coding Model: Domain-specific coding models and evals to expedite system software workflows like code development, integration, debugging, porting & documentation, for automotive, electronics, IoT, telecom, avionics, and semiconductors sector offering
- 50 – 200% improvement in system Engineering team velocity
- 10-20x return on investment
- 80% reduction in documentation efforts
- 10x faster legacy system discovery, porting & Integration

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