



Dear Patrons,

Warm New Year wishes to you and your families. As we enter 2026, IESA concluded the year on a strong note, showcasing steady progress and impactful momentum across policy advocacy, industry collaboration, startup engagement, academia, and global partnerships.

December marked a strong close to the year, with IESA engaging in a closed-door leadership meeting with the **Department of Science & Technology (DST)** focused on the Research, **Development, and Innovation (RDI) Scheme**. The discussion reinforced IESA's role in enabling industry collaboration, IP creation, and commercialization under India's landmark ₹1 lakh crore RDI Fund. This was followed by IESA's collaboration with **Analog Devices (ADI)** to host a **high-level Leadership Roundtable**, bringing together senior leaders from industry, startups, academia, and the semiconductor ecosystem to deliberate on ecosystem maturity, global shifts, and building a trusted semiconductor value chain.

IESA Bengaluru hosted two landmark initiatives. The **IESA Mid-Year Member Meet 2025** convened leaders from across India's ESDM and semiconductor ecosystem, featuring perspectives from **Navin Bishnoi**, **Dr. Sanjay Tyagi (STPI)**, and **Mr. Ashok Chandak**, along with policy and R&D insights from **Dr. S.D. Sudarsan (C-DAC)** and a special address by **Kamaljeet Singh, Director General, SCL Mohali**, and **Dr. Jyoti Sharma (DST – RDI Cell)**. On the same day, the **IESA Startup Mitra Conclave 2025** brought together **100+ ESDM startups, investors, and policymakers**, with VC insights moderated by **Pradeep Vajram**, startup journeys from **InCore Semiconductors** and **EVQPoint Solutions**, and perspectives from KITS and SFAL.

Beyond India, IESA maintained strong global engagement through participation in the **India-Japan Semiconductor Forum** at **SEMICON Japan 2025**, the **SEMI International Policy Summit (SIPS) 2025** in Singapore, and the **India-Canada Sectoral Webinar** on ESDM and the Global MSME Summit 2025.

December also witnessed a strong focus on academia-industry collaboration. IESA successfully concluded its first **Academia-Industry Visit to Bengaluru (11-13 December 2025)**, bringing together faculty from institutions across India for exposure to IISc Bengaluru, GlobalFoundries, Tessolve Semiconductor, and the IESA Office. These engagements strengthened PAN-India collaboration and reinforced IESA's commitment to building an industry-ready semiconductor workforce.

This month, we also enclose the special supplement on the **IESA's Midwest chapter activities led by Mr. Sudhir Naik**, Ex EC Member, in promoting IESA's role in the region.

Together, these initiatives underscore IESA's continued commitment to advancing India's ESDM and semiconductor ecosystem through collaboration, capability building, and global alignment.



Yours Sincerely
Ashok Chandak
President, IESA



Yours Sincerely
Ruchir Dixit
Chairman, IESA

HAPPY NEW YEAR

IESA Media & PR Dashboard: December Highlights

Overall Media Reach (Metric Coverage)

Total Media Stories	166
Print	110
Online	52
TV Interview	4

Tier -I/ TV Media Visibility

Platform	Engagement
NDTV Profit	Exclusive Interaction
CNBC- TV 18	Panel opportunity
ET Swadesh	Exclusive Panel Discussion

Policy & Industry Narrative

Topic	Platform
Chip and EMS Companies under ISM 2.0	Economic Times
Industry stories	Mint
Chinese Joint Ventures	Economic Times

Regional Media Impact

Focus Area	Coverage
Structural Growth Narrative	NDTV Profit, ET Now
ECMS & RDI	Trinity Mirror, Biswabani
Cabinet Landmark	IANS, DataQuest, Lokmat Times, and Business News
Industry Stories	ET Manufacturing Mint, and Free Press Journal

Social Media Growth

Platform	Performance
LinkedIn	+602 (Total 34,795)
Twitter/ X Engagement	1900 impressions, 117 engagements, 26 followers

Key Highlights

- IESA's media engagement significantly strengthened policy visibility, industry credibility, and national and regional reach—reinforcing IESA as a trusted voice in India's ESDM and semiconductor ecosystem.
- Ashok Chandak, President IESA, continues to serve as a key spokesperson for the ESDM industry and IESA, effectively articulating industry perspectives across leading national and regional media platforms.

[View PR Coverage Summary](#)

IESA Engages with DST Leadership on RDI Scheme to Catalyse India's Innovation Ecosystem

IESA participated in a **closed-door leadership interaction** at DST India on the RDI Scheme, engaging with **Prof. Abhay Karandikar**, Secretary, DST, and **Dr. Jyoti Sharma**, Head – RDI Cell, DST. **Mr. Ashok Chandak**, President, IESA, along with IESA member leaders from NXP Semiconductors, L&T Semiconductor Technologies, HCLTech, TCS, and SyrmaSGS, shared industry perspectives on advancing deep-tech, semiconductor, and R&D innovation. The interaction underscored IESA's role in enabling industry collaboration, IP creation, and commercialization under India's landmark ₹1 lakh crore RDI Fund.

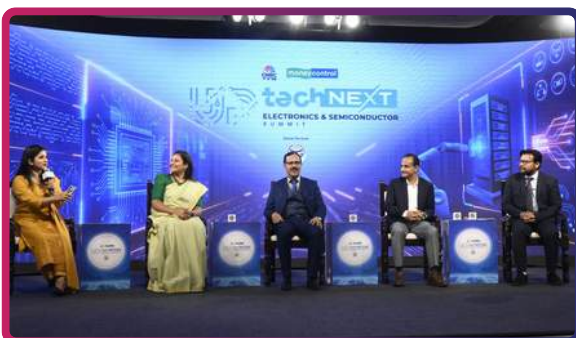


SEMI International Policy Summit 2025, Singapore

IESA participated in the **4th SEMI International Policy Summit (SIPS)** held in **Singapore on 11–12 December 2025**, which brought together global leaders from industry, government, and academia to address key challenges in the semiconductor sector. The summit saw strong participation from India, with insights from **Mr. S. Krishnan**, Secretary, MeitY, alongside representatives from JS MeitY, JS MEA, and IESA. Discussions highlighted India's policy leadership, public-private collaboration, and growing trust in the global ESDM landscape.

IESA's Leadership Roundtable with Analog Devices Highlights

On **12 December**, IESA, in collaboration with **Analog Devices (ADI)**, hosted a high-level **Leadership Roundtable** bringing together senior leaders from industry, academia, startups, and the semiconductor ecosystem. The interaction featured **Mr. Vincent Roche**, President & CEO, Analog Devices, **Mr. Ray Stata**, Founder, Analog Devices, **Mr. Vivek Tyagi**, MD, Analog Devices India and other leaders too. Discussions underscored India's growing ecosystem maturity, global industry shifts, and aligned efforts toward building a trusted, globally competitive semiconductor value chain.



IESA Unveils UP State Growth Roadmap for Electronics & Semiconductors at UP Tech Next Summit

On 03 December, at the **UP Tech Next Summit** hosted by Network18 and Moneycontrol, IESA presented the UP State Growth Roadmap for Electronics & Semiconductors to **Shri Sunil Kumar Sharma, Hon'ble Minister for IT & Electronics, Government of Uttar Pradesh**. The roadmap was formally released by **Mr. Ashok Chandak, President, IESA & SEMI India**, and **Mr. Vivek Tyagi**, IESA EC Member, outlining UP's strategic potential across semiconductors, ESDM manufacturing, talent, and policy-driven ecosystem growth. The event was telecast by CNBC TV channel.

Highlights From IESA Mid-Year Member Meet 2025

On 04 December, IESA convened the **Mid-Year Member Meet 2025** in Bengaluru as a key milestone to review progress, strengthen partnerships, and realign priorities for India's ESDM and semiconductor ecosystem. The meet served as a strategic platform for **industry, government, academia, and startups** to converge on India's vision of becoming a product and production nation. Discussions focused on talent development, research, innovation, policy alignment, and ecosystem building, reaffirming a shared commitment to a resilient, innovation-led ESDM future. More than **170 participants** attended the meeting and networking.



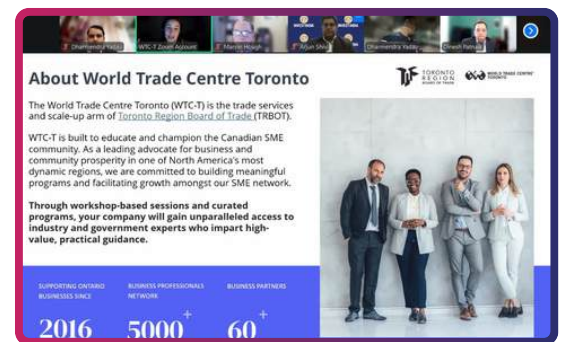


IESA Startup Mitra Conclave 2025 Accelerates ESDM Startup Collaboration

The **IESA Startup Mitra Conclave 2025**, held on **04 December** in Bengaluru, brought together **100+ ESDM startups**, investors, and policymakers to strengthen India's startup ecosystem. Designed as a convergence platform for capital, policy, and innovation, the conclave featured VC insights, startup-focused policy discussions, and success stories from **InCore Semiconductors** and **EVQPoint Solutions**. A special address by **Kamaljeet Singh**, Director General, **SCL Mohali**, highlighted India's indigenous semiconductor roadmap, reinforcing collaboration-led growth across the ESDM ecosystem. EC member **Akshay Agarwal**, **Pradeep Vajram**, **Sundeeb Gupta**, along with **Shakar Krishnamurthy**, put together the great Start Up Mitra activity.

IESA at India-Canada Sectoral Webinar on ESDM and Advanced Manufacturing

IESA participated in the **India-Canada Sectoral Webinar on ESDM and Advanced Manufacturing** held on **10 December 2025**. **Mr. Ashok Chandak**, President, IESA, joined as a keynote speaker, sharing insights on India's rapidly growing electronics and semiconductor landscape, including PLI incentives and market-entry opportunities for Canadian firms. The session highlighted collaboration models across electronics, semiconductors, and advanced manufacturing, reinforcing IESA's role in enabling cross-border partnerships between India and Canada's innovation ecosystems.



Strengthening MSME Growth and Digital Readiness: IESA at Global MSME Summit 2025

IESA participated in the **Global MSME Summit 2025**, held on **December 5** at the **Manipal Academy of Higher Education (MAHE), Bengaluru**, focusing on Resilient Growth & Global Expansion for MSMEs. Organised by **AWAKE** and **CIMSME**, the summit spotlighted digital transformation, fintech-driven funding, and global market access. **Mr. Sanjeev Kesar**, IESA Member, underscores the role of technology, resilience, and ecosystem collaboration in enabling MSMEs to scale sustainably and compete on the global stage.

Highlights from GCCI Education Committee Session in Ahmedabad

On **08 December**, **IESA** participated in the GCCI Education Committee session, "Semiconductor Landscape: Global Trends, India's Roadmap and Industry Opportunities for Gujarat", held at GCCI, Ahmedabad. **Shri Sudhir Naik**, Midwest Head – IESA and CEO & Founder, Ncube Semicon, shared perspectives on global semiconductor trends, India's policy roadmap, and Gujarat's emerging role across design, manufacturing, and innovation. The session brought together academia, industry, and innovators to explore Gujarat's growing potential in the global semiconductor landscape.



IESA at SEMICON Horizon 2025: Shaping the Future of VLSI in India

SEMICON Horizon 2025, the **1st International Conference on Semiconductor Technologies and Smart Designs**, was held on **19 December 2025**, bringing together industry experts and academia to discuss the future of semiconductor innovation. **Mr. Sudhir Naik**, representing **IESA**, delivered a keynote address on "Future of VLSI in India Semiconductor Mission", sharing insights on India's evolving chip design landscape, talent development, and ecosystem readiness. The conference was co-hosted by **Parul University** and **Elfonchips**, reinforcing strong industry-academia collaboration.

Highlights from IESA's 3-Day Academia-Industry Visit to Bengaluru

IESA successfully concluded its first **Academia-Industry Visit to Bengaluru (11-13 December 2025)**, bringing together **24 faculty members from 11 institutions** across Noida, Ahmedabad, Kottayam, Chennai, Coimbatore, Vellore, and Hosur.

As part of IESA's Workforce Development (WFD) initiatives, led by Viswanathan KG, Director – Workforce Development, IESA, participants visited **IISc Bengaluru, GlobalFoundries, Tessolve Semiconductor, and the IESA Office**, gaining valuable exposure to advanced research, semiconductor manufacturing, VLSI design, testing, and product engineering.

The engagement, supported by **Kalpana Subbaramappa, Dibyendu Ghosh, Sudhakar Rajamanickam, and Dr. V. Veerappan**, strengthened PAN-India academia-industry collaboration, enhanced understanding of real-world industry workflows, and reinforced the critical role of educators in building an industry-ready semiconductor workforce.



IESA Organises Academia-Industry Visit in Chennai to Strengthen Workforce Development

On 17 December, IESA conducted an **Academia-Industry Visit** in Chennai, facilitating an engagement between faculty members from SRM Institute of Science and Technology (SRM IST) and the Technical Wing of Elektronika Sales Private Limited.

As part of IESA's Workforce Development initiatives, the visit provided valuable insights into real-world electronics applications, industry-led design challenges, and current workforce expectations. The initiative, led by **Viswanathan KG, Director – Workforce Development**, IESA, enabled productive interactions on emerging technology trends and reinforced the importance of sustained collaboration between academia and industry to build a robust and future-ready electronics and semiconductor talent ecosystem.



IESA Collaborates with Japanese Partners to Advance ESDM Future

IESA entered into a strategic collaboration with **Elematech Japan, Niterra Japan, and Global Marketing Services** to strengthen future opportunities in India's **ESDM and semiconductor ecosystem**. The engagement focused on fostering cross-border partnerships, technology exchange, and industry-led collaboration to support manufacturing, design, and supply-chain development. This collaboration marks an important step in deepening India-Japan cooperation and reinforces IESA's role in enabling global partnerships to accelerate innovation and sustainable growth across the ESDM value chain.

IESA Hosts Skill Development Webinar on India's Fabless Future

IESA hosted the skill development webinar, "India's Fabless Future: Opportunities for Engineering Students", bringing together students and aspiring engineers from across the semiconductor ecosystem. The session was led by **Mr. Viswanathan K.G.**, IESA Leader, with expert insights from **Mr. Nilesh Ranpura**, Sr. Director Engineering – ASIC, eInfochips (An Arrow Company), and **Ms. Padma Priya P.**, Director & Country Head – HR, Blaize. The webinar with **100+ attendees** provided clear guidance on roles, skills, and career pathways, strengthening job readiness in India's growing fabless sector.



UPCOMING EVENTS

- WorkForce Development Webinar, 21 January
- IESA Mid-West Chapter Meet, 24 January
- IESA NCR Chapter Meet, 4 February
- IESA-NXP Leadership Forum Roundtable, 12 February
- Webinar on "Addressing Challenges of Impurities in the Semiconductor Supply Chain", 19 February

VISION SUMMIT

Save the Date

25-26 FEBRUARY

StartUp / MSMEs of the Month - January 2025

SmowCode[®]

Company Name: Smowcode (pvt ltd)

Location: Andheri west, Mumbai

Founder: Adhik Mewada

Website: www.smowcode.com

Email ID: adhik@smowcode.com

Overview:

Industry Grade Graphical Coding Language, IDE and AI

Key Products/ Services/ Highlight:

Products:

- Smowcode IDE
- Smowcode Compiler
- Smowcode Scarlet AI

Features:

- Confine coding efforts to only logic creation.
- Make 30 days of code in just 3 days.
- IoT Ready - It supports the powerful ESP32 micro-controller chip series.
- Smart libraries that connect with each other seamlessly and work in harmony
- without any management from user's end.
- Wide applications - Control (motor, pneumatic cylinders etc), Instrumentation (measure temperature, liquid pressure etc), IoT (send/receive information via any protocol), Data processing (manipulation, mapping, calculations, algorithm etc)

IESA Offices

Bengaluru (Head Office):

India Electronics & Semiconductor Association
No.16, Kensington Road Cross, Next to RMZ
Millenia Tower, Ulsoor, Bangalore 560008
☎ 080 40939022, 080 40939918

Delhi:

Indo-French Chamber of Commerce & Industry (Delhi)
Gate no. 3, 1st floor, R K Khanna Tennis Stadium,
1, Africa Ave, near Deer Park, Safdarjung Enclave,
New Delhi, Delhi 110029, ☎ 080 40939022,
080 40939918

Ahmedabad:

2, Aryan Park Complex Nr,
Railway Crossing,
Thaltej - Shilaj Rd, Thaltej,
Ahmedabad, Gujarat 380054

www.iesaonline.org